A Neural Approach for Compensation of Effects of PAPR Causing BER Degradations in Hiperlan2

Shubhangi Rathkanthiwari, Chandrashekhar Dethe, and Kishore Kulat

Abstract—Emergent telecommunication systems use spectral efficient multilevel modulation formats together with multicarrier schemes such as Orthogonal Frequency division multiplexing (OFDM). OFDM transmission is an efficient way to deal with multipath. However one of its major drawbacks is its sensitivity to nonlinear distortions due to its greatly variable envelope and high peak to average power ratio (PAPR). Nonlinear distortions are mainly introduced by high power amplifiers (HPA), cause in band distortions. Misalignments in IQ modulators and demodulators causes imbalance, which results in loss of orthogonality and create intercarrier interference and spectral regrowth in an OFDM system. Nonlinear distortions also cause intermodulation effect. This degrades Bit error rate (BER) performance of the system. This paper introduces the self organization map(SOM) and parameterless self organization map(PLSOM) based and Hiperlan2 receiver structure used for the compensation of nonlinear distortions. Simulation results presented in this paper clearly indicate the improved performance of the proposed system. Simulations are run for AWGN channel model and for two HPA models namely Travelling wave tube amplifier (TWTA) and Solid state power amplifier (SSPA), with and without SOM and PLSOM neural network blocks.

Index Terms—OFDM; HPA; TWTA; SSPA; BER; AWGN; SOM; PLSOM

I. INTRODUCTION

Wireless LAN standards such as Hiperlan2 and 802.11a use OFDM as a multi-carrier modulation/multiplexing technique. OFDM transmission exhibits many benefits in wireless communications. An OFDM signal is the sum of sinusoids transmitted through multicarriers, so that peak power of the OFDM signal increases in proportion to the number of subcarriers. In OFDM systems, the combination of different signals with different phase and frequency gives a large dynamic range that is used to be characterized by a high PAPR, which results in severe clipping effects and nonlinear distortion if the composite time signal is amplified by a power amplifier which have nonlinear transfer function. This degrades the performance of an OFDM system [1-3]. As a result, multicarrier systems are more sensitive to presence of nonlinearities. Due to large dynamic range of the modulated signal, nonlinear distortion at the power amplifier causes interference both inside, resulting in intermodulation among the subcarriers as well as outside, which causes spectral spreading of OFDM signal. In band distortion may result in intermodulation, which is a mutual interference between signals spaced apart in frequency, after nonlinear amplification of signal by HPA. This causes degradation in Bit error rate (BER) performance of the system. Out of band components affect adjacent frequency band components [4]. A measure of the degradation can be very helpful in evaluating the performance of a given system, and in designing a signaling set that avoids degradation. This paper presents the performance of Hiperlan2 with power amplifiers to study the effects of nonlinearities on the received signal. Back off strategies will then be used to minimize the effects of nonlinear distortions on the signal constellation of 16 QAM signal.

TWTA and SSPA models are most commonly used power amplifiers in wireless communication systems. To achieve maximum output power efficiency, HPA is usually operated in the saturation region, which is basically a nonlinear region. As OFDM signal is characterized by high PAPR and large dynamic variation of the signal amplitude, it is highly affected by nonlinear distortion [5]. Basic Hiperlan2 model to be simulated is shown in Figure 1

From fig. 1, first block is Bernauli binary generator block that provides the information source for the simulation. Convolutional code block creates convolutional code for the binary data. It encodes the sequence of binary input vectors to produce a sequence of binary output vectors. Puncture block carries out puncturing. It periodically removes bits from the encoded bit stream thereby increasing the code rate. Puncture pattern is specified by the puncture vector parameter in the mask. Matrix interleaver block permutes input symbols by filling a matrix by rows and emptying it by columns. General block interleaver block rearranges the elements of its input vector without repeating or omitting any elements. Rectangular QAM modulator base band block modulates using M ary QAM with a constellation on a rectangular
lattice. Normalize block normalizes the rectangular QAM signal which is fed to the OFDM transmitter block. Discrete signal scatter plot scope is used to display a modulated signal constellation in its signal space by plotting the in phase component versus quadrature components. An OFDM symbol is a sum of N independent symbols mapped on N different sub-channels with 1/T frequency separation. T is the OFDM symbol period. Discrete time domain samples b — components of the OFDM signal with magnitude and angle components are converted to the complex signal and output signal. The next combiner block combines the new results to the signal to produce the output signal. AM/PM conversion block applies an AM/PM conversion to the phase of the signal, according to the linear gain parameter, which is controlled by the selected interpolation method, and the adder block add the result to the angle of the signal to produce the magnitude of the output signal. The next combiner block combines the new magnitude and angle components into a complex signal and multiplies the result by a gain factor, which is controlled by the selected interpolation method, to produce the magnitude of the output signal. The selected interpolation method, to produce the magnitude of the output signal. The next combiner block combines the new magnitude and angle components into a complex signal and multiplies the result by a gain factor, which is controlled by the linear gain parameter.

First block multiplies the signal by a gain factor. Second block splits the complex signal into its magnitude and angle components. AM/AM conversion block applies an AM/AM conversion to the magnitude of the signal, according to the selected interpolation method, to produce the magnitude of the output signal. AM/PM conversion block applies an AM/PM conversion to the phase of the signal, according to the selected interpolation method, and the adder block add the result to the angle of the signal to produce the magnitude of the output signal.

For Saleh method, plots of
- Output voltage against input voltage for the AM/AM conversion
- Output phase against input voltage for the AM/PM conversion

Figure 3 shows, for the Saleh method, plots of
- Output voltage against input voltage for the AM/AM conversion
- Output phase against input voltage for the AM/PM conversion

For Saleh Model, the input scaling (dB) parameter scales the input signal before the nonlinearity is applied. The block multiplies the input signal by the parameter value, converted from decibels to linear units.

The AM/AM parameters, alpha and beta, are used to compute the amplitude gain for an input signal using the following function

\[ F_{AM/AM(u)}(u) = \frac{\text{alpha} \cdot u}{1 + (\text{beta} \cdot u^2)} \]  

Where u is the magnitude of the scaled signal. The AM/PM parameters, alpha and beta, are used to compute the phase change for an input signal using the following function.
where $u$ is the magnitude of the input signal. Note that the AM/AM and AM/PM parameters, although similarly named alpha and beta, are distinct. The Output scaling (dB) parameter scales the output signal similarly. For Rapp Model, the Smoothness factor and Output saturation level parameters are used to compute the amplitude gain for an input signal by the following function

$$F_{AM/PM}(u) = \frac{\alpha u^2}{1 + (\beta u^2)}$$  \hspace{1cm} (9)$$

where $u$ is the magnitude of the scaled signal, $S$ is the Smoothness factor and $O_{sat}$ is the Output saturation level. The Rapp model does not apply a phase change to the input signal. Common operating parameters of HPA are shown in figure 4 [8].

$$F_{AM/AM}(u) = \frac{u}{1 + \left( \frac{u}{O_{sat}} \right)^{2 \Delta f/2\pi}}$$  \hspace{1cm} (10)$$

where $u$ is the magnitude of the input signal, $S$ is the Smoothness factor and $O_{sat}$ is the Output saturation level.

Back off may be calculated from

1) Average signal power and saturated signal power at the output. This is Output back off (OBO).

$$OBO_{dB} = -10 \log_{10} \left( \frac{E\{V_{sat}^2\}}{P_{sat,OUT}} \right)$$  \hspace{1cm} (11)$$

2) Average input signal power level and input power level corresponding to the saturation in the linearized model. This is Input back off (IBO).

$$IBO_{dB} = -10 \log_{10} \left( \frac{E\{V_{IN}^2\}}{P_{sat,IN}} \right)$$  \hspace{1cm} (12)$$

Neural networks are able to give solutions to complex problems in digital communications due to their nonlinear processing, parallel distributed architecture, self-organization, capacity of learning and generalization, as well as efficient hardware implementation [9]. Here the novel architecture is proposed for compensation of nonlinear distortions as shown in figure 5.

Nonlinearity received is in time domain. Neural network is in frequency domain. Neural block is therefore placed after FFT block. Neural network architectures under consideration are SOM and PLSOM neural blocks.

SOM consists of a series of neurons that act upon a series of inputs. Each neuron is densely interconnected, which receives input signal, a great number of lateral interconnections from output of other units. The primary unit determines a winner node, which will have a certain cluster, and then following the input, the winner node with its surrounding neighborhood adapts to the input. This process continues for certain number of iterations until a certain degree of adaptations is reached.

To compensate for the nonlinearities, SOM algorithm is used after FFT block at the receiver side. Nonlinearity from HPA is in time domain, whereas SOM algorithm is in frequency domain. SOM algorithm is introduced in the following steps.

1) Initialize the values of nodes $\omega_j$ by using values of ideal 16-QAM signal constellations. We design SOM neurons as the same as the constellation of 16-QAM. The two dimensional weight coefficients of the neuron equal to in-phase and quadrature components of the point in 16-QAM constellations

2) Locate the winning node $\hat{i}$ for the input signal $I$ by

$$\|\hat{I}(k) - \omega_i\| = \min_j \left\{ \|I(k) - \omega_j\| \right\}$$  \hspace{1cm} (13)$$

After locating the winning node $i$, we use the Euclidean distance as neighborhood radius to define neighborhood of winning node $i$.

3) Modify the values of the winning node to the direction of the input data and modify the values of the neighbors of the winning node in the same way. The neighborhood function $h_{j,i}$ is defined by

$$h_{j,i} = \exp \left(-\frac{d_{j,i}^2}{2\sigma^2}\right)$$  \hspace{1cm} (14)$$

where $d_{j,i}$ is the neighborhood radius and the parameter $\sigma$ is the effective width of the topological neighborhood. To satisfy the requirement that the size of the topological neighborhood shrinks with time, we let the width $\sigma$ of the topological neighborhood function $h_{j,i}$ decrease with time.

$$\sigma(n) = \sigma_0 \exp \left(-\frac{n}{\varphi}\right)$$  \hspace{1cm} For n = 0, 1, 2 \hspace{1cm} (15)$$
\( \sigma_0 \) is the value of \( \sigma \) at the initiation of SOM algorithm. \( \varphi_1 \) is a time constant. The topological neighborhood assumes a time varying form of its own and is given by

\[
h_{j,i}(n) = \exp\left(-\frac{d_{j,i}^2}{2\sigma_0^2(n)}\right)\quad (16)
\]

Thus as the time/number of iterations increases, width \( \sigma(n) \) decreases at an exponential rate and the topological neighborhood shrinks in a corresponding manner.

For 16-QAM system, \( \sigma = 2 \) and \( \varphi_1 = \frac{1000}{\log \sigma_0} \)

For a network to be self-organizing, synaptic weight vector \( \omega_j \) of neuron \( j \) in the network is required to vary in relation to the input \( I(k) \). Increment of the weight vector of neuron \( j \) is

\[
\Delta \omega_j = \eta h_{j,i}(\Lambda - \omega_j)\quad (17)
\]

where \( \eta \) is a scalar valued adaptation gain and should be decreased with time.

\[
\eta(n) = \eta_0 \exp\left(-\frac{n}{\varphi_2}\right)\quad (18)
\]

Where \( \varphi_2 \) is another time constant of SOM algorithm. Even though the neighborhood function and learning rate parameter may not be optimal, they are usually adequate for the formation of feature map in a self-organized manner.

4) For the input signal \( I(k) \) the output is defined by the modified weight \( \omega_j(k + 1) \) and reset the constellation by weight vector \( \omega_j(k + 1) \).

Unfortunately, this unsupervised learning is dependent on two annealing schemes, one for the learning rate and one for the neighborhood size. Parameterless self-organizing map is a neural network, based on SOM that eliminates need for a learning rate and neighborhood size [10].

PLSOM algorithm:

Fundamental idea of PLSOM is that the amplitude and extent of weight updates are not dependent on iteration number, but how good the fit is, we calculate a scaling variable \( \varepsilon \) which is then used to scale the weight update, which can be defined through following equations (19) and (20).

\[
e(t) = \frac{\|x(t) - w_e(t)\|}{r(t)}\quad (19)
\]

\[
r(t) = \max(\|x(t) - w_e(t)\|, r(t - 1)\)
\]

\[
r(0) = \|x(0) - w_e(t)\|\quad (20)
\]

\( e(t) \) is best understood as the normalized Euclidean distance from the input vector at time \( t \) to the closest weight vector. If this variable is large, network fits the input data poorly and needs a large adjustment. Conversely, if \( \varepsilon \) is small, the fit is likely to already be satisfactory for that input and no large update is necessary. Algorithm for PLSOM uses a neighborhood size determined by \( \varepsilon \) thus replacing the equation governing the annealing of neighborhood with \( \beta(t) = \text{constant} \forall t \).

\( \beta \) is scaled by \( \varepsilon(t) \), giving \( \Theta(\varepsilon(t)) \) the scaling variable for neighborhood function (21)

\[
\Theta(\varepsilon(t)) = \beta(t)\quad \text{where}
\]

\[
\Theta(\varepsilon(t)) \geq \theta_{\min}\quad (21)
\]

Second option for calculating \( \Theta(\varepsilon(t)) \) is given in eqn (22).

\[
\Theta(\varepsilon(t)) = (\beta - \theta_{\min})\varepsilon(t) + \theta_{\min}\quad (22)
\]

Third alternative is equation (23).

\[
\Theta(\varepsilon(t)) = (\beta - \theta_{\min})\ln(1 + \varepsilon(t)(e - 1)) + \theta_{\min}\quad (23)
\]

Here, \( \ln(\cdot) \) is the natural logarithm, \( e \) is the Euler number, \( \theta_{\min} \) is some constant, whose value is taken as 1 for the equations (9) and (10), and 0 for the equation (11). Neighborhood function is given by,

\[
h_{c,i}(t) = \frac{-d(i,c)^2}{e^{\theta(x(t))}}\quad (24)
\]

\( d(i,c) \) is the distance measure along the grid in the output space from the winning node \( c \) to \( i \), which is the node we are currently updating. This gives a value, that decreases further we get from \( c \) and the rate of decrease is determined by \( \varepsilon \). Weight update functions are

\[
w(t + 1) = w(t) + \Delta w(t)\quad (25)
\]

\[
\Delta w(t) = \varepsilon(t)h_{c,i}(t)[x(t) - w_i(t)]\quad (26)
\]

Learning rate \( \alpha(t) \) is now completely eliminated, replaced by \( \varepsilon(t) \). The size of update is not dependent on the iteration number. The only variable, affecting the weight update which is carried over between the iterations is the scaling variable \( r(t) \).

SIMULATIONS:

Simulations are carried out using MATLAB SIMULINK model 16 QAM modulation schemes for ¾ code rate for AWGN channel. Hiperlan/2 Simulink model with TWTA (Saleh model) and SSPA (Rapp’s model) has been simulated for four cases.

1) Hiperlan/2 without neural block with AWGN channel
2) Hiperlan/2 without neural block with Rayleigh fading channel
3) Hiperlan/2 with SOM block
4) Hiperlan/2 with PLSOM block

The function newsom creates a self organizing map. This returns a new SOM. For this paper, SOM is trained with 1000 epochs and the input vectors are plotted with the map that formed the SOM weights. SOM consists of a single layer with ‘negdist’ weight function, ‘netsum’ net input
function and ‘compete’ transfer function. The layer has a weight from the input but no bias. The weight was initialized with ‘midpoint’. Adaptation was done with the function ‘trainr’ and the weights were updated with the algorithm ‘learnson’. The distance function used is ‘dist’ which calculates the Euclidean distance from home neuron to any other neuron. All the neighborhoods for a S neuron layer map are represented by S X S matrix of distances. ‘Gridtop’ topology starts with the neuron in the rectangular grid. Weights of winning neurons are adjusted using Kohonen’s rule. The rule allows the weights of neurons to learn an input vector. Thus the neuron, whose weight vector was closest to the input vector was updated to be even closer.

PLSOM completely eliminates selection of learning rate, the annealing rate and the annealing scheme of learning rate and the neighborhood size. It also decreases the number of iterations required to get a stable and ordered map. It has been proved that in 600 epochs we are getting the desired result.

Simulation setting for getting BER plot for Hiperlan/2 with Saleh model is given in table 1 and the BER plot is shown in figure 7.

TABLE 1: SIMULATION PARAMETERS FOR HIPERLAN/2 WITH SALEH MODEL

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value/ Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>WLAN Model for simulation</td>
<td>Hiperlan/2</td>
</tr>
<tr>
<td>Modulation scheme</td>
<td>16 QAM</td>
</tr>
<tr>
<td>Code rate</td>
<td>3/4</td>
</tr>
<tr>
<td>Puncture vector</td>
<td></td>
</tr>
<tr>
<td>Number of sub carriers</td>
<td>52</td>
</tr>
<tr>
<td>FFT size</td>
<td>64</td>
</tr>
<tr>
<td>GI length</td>
<td>16</td>
</tr>
<tr>
<td>Channel model</td>
<td>AWGN/ Rayleigh fading</td>
</tr>
<tr>
<td>AM/AM parameter alpha</td>
<td>2.1587</td>
</tr>
<tr>
<td>AM/AM parameter beta</td>
<td>1.1517</td>
</tr>
<tr>
<td>AM/PM parameter alpha</td>
<td>4.0033</td>
</tr>
<tr>
<td>AM/PM parameter beta</td>
<td>9.1040</td>
</tr>
</tbody>
</table>

Simulation setting for getting BER plot for Hiperlan/2 with Rapp’s model is given in table 2 and the BER plot is shown in fig. 7.

Fig. 7. BER plot for Hiperlan/2 with Rapp’s model

II. CONCLUSION

This paper presents results of simulations run on neural network based Hiperlan/2 WLAN receiver structure in fading multipath environment. Hiperlan/2 uses OFDM as multiplexing scheme, which has a main drawback of having high peak to average power ratio. Most of the signal components are amplified in saturation region of the high power amplifiers used at the transmitter end causing nonlinear distortions. For this paper, simulations are run for Hiperlan/2 with HPA models like Saleh model (TWTA) and Rapp’s model (SSPA). Modulation method is 16 QAM, and code rate is 3/4. Channel models are AWGN and Rayleigh fading. SOM and PLSOM blocks act as adaptive decision devices that not only compensate for nonlinear distortions but also follow up error signals. BER vs SNR plots clearly indicate the better performance of new neural based Hiperlan/2 WLAN receiver structure.

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